سامية محمد مصطفى

شبكة المعلومات الجامعية

بسم الله الرحمن الرحيم

سامية محمد مصطفى

شبكة المعلومات الجامعية

شبكة المعلومات الجامعية

التوثيق الالكتروني والميكروفيلم

سامية محمد مصطفى

شبكة المعلومات الجامعية

جامعة عين شمس

التوثيق الإلكتروني والميكروفيلم

قسم

نقسم بالله العظيم أن المادة التي تم توثيقها وتسجيلها علي هذه الأقراص المدمجة قد أعدت دون أية تغيرات

يجب أن

تحفظ هذه الأقراص المدمجة بعيدا عن الغبار

سامية محمد مصطفى

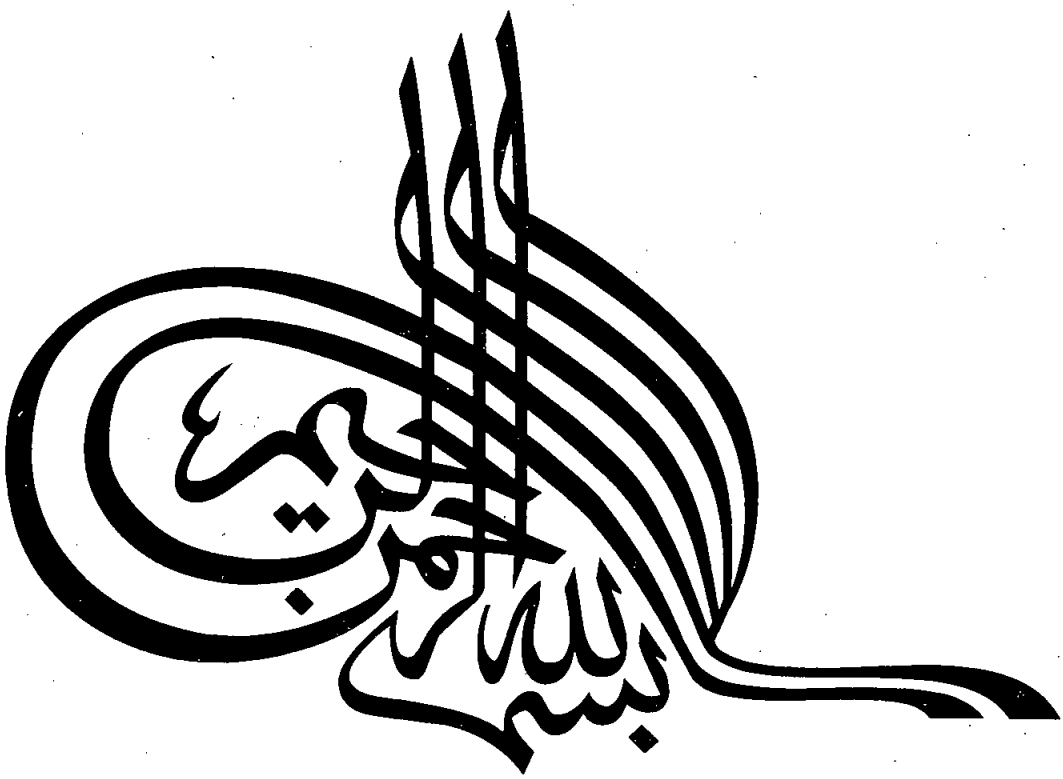
شبكة المعلومات الجامعية

بعض الوثائق الأصلية تالفة

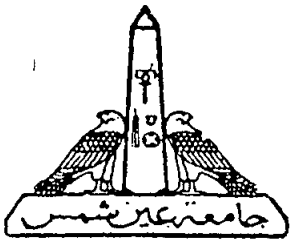
سامية محمد مصطفى

شبكة المعلومات الجامعية

بالرسالة صفحات لم ترد بالأصل



B
١٤٢٧٦



Ain Shams University
Faculty of engineering
Computer and Systems Engineering Department

Formal Verification Tool for Variable Order Analysis Algorithm in Binary Decision Diagrams

A Thesis

Submitted in Partial Fulfillment of the
Requirements of the Degree of
Master of Science in Electrical Engineering
(Computer Engineering)

Submitted by

Mohamed Mahmoud Ahmed Taher

B. Sc., Electrical Engineering
(Computers & Systems Engineering)
Ain Shams University, 1996

Supervised By

Prof. Dr. Abdel Moniem Wahdan
Prof. Dr. Hani M. K. Mahdi
Dr. Ashraf El-Farghly Salem

Cairo 2001

Statement

This dissertation is submitted to Ain Shams University for the degree of Master of Science in Electrical Engineering (Computer Engineering).

The work included in this thesis was carried out by the author at the Computer and Systems Engineering department, Faculty of Engineering, Ain Shams University.

No part of this thesis has been submitted for a degree or qualification at other university or institution.

Name Mohamed Mahmoud Ahmed Taher

Signature

Date

Approval Sheet

Name Mohamed Mahmoud Ahmed Taher

Thesis Formal Verification Tool for Variable Order Analysis Algorithm in Binary Decision Diagrams

Degree Master of Science in Electrical Engineering (Computer and Systems Engineering)

Examiners Committee

Name, Title and Affiliation **Signature**

1. Prof. Dr. Mohamed Adeeb Reyad Ghonaimy
   Computer & Systems Engineering Department
   Faculty Of Engineering
   Ain shams University, Cairo.

2. Prof. Dr. Magdy Fekry Ragaay .....
   Electronic & Communication Engineering Department
   Faculty Of Engineering
   Cairo University,CAIRO.

3. Prof. Dr. Abdel-Moniem A. Wahdan
   Computer & Systems Engineering Department
   Faculty Of Engineering
   Ain shams University, Cairo.

4. Prof. Dr. Hany Kamal Mahdi
   Computer & Systems Engineering Department
   Faculty Of Engineering
   Ain shams University, Cairo.

Date : 29/4/2001

Acknowledgement

I sincerely thank Prof. Dr Abel Moniem Wahdan for accepting me as his student and for giving me the chance to work in my interested research point. I owe a special acknowledgment to him for his valuable advices. I am very pleased to work under a supervision of him.

I am also quite grateful for Professor Hani Mahdi for being one of my supervisors and for his interest in my work. I would like to thank him for spending a lot of his precious time reviewing my work.

I am deeply grateful to Dr. Ashraf Salem for introducing me to the field of formal verification, suggesting the research point, spending a lot of his time reading the thesis, writing his criticism, suggesting improvement and helping me to have a very clear thesis. I was very pleased when I published my first paper with him and Prof. Hani and Prof. Wahdan at ICECS'2000 in Lebanon.

I would like to thank my colleagues of Computer and Systems Engineering Department. for their support and encouragement. I would like to give special thanks to Eng. Mohamed Sabry for helping me in writing this thesis. I wish him all the best.

Finally, I would like to thank my parents and my lovely fiancée for their support and encouragement during the most important stages of the thesis.