



# Ain Shams University Faculty of Engineering Computers and Systems Engineering Department

621.38195 R.S Hig Scheduling in

in
High-Level Synthesis

A Thesis Submitted in Partial Fullfillment of The
Requirements of The Degree of Master of Science in
Electrical Engineering
(Computers and Systems Engineering)

Submitted by

Eng. Rami Said Abbas

Bachelor of Computers and Systems Engineering Computers and Systems Engineering Department Faculty of Engineering - Ain Shams University 1992

Cairo - 1997







### Ain Shams University Faculty of Engineering Computers and Systems Engineering Department

# Scheduling in **High-Level Synthesis**

A Thesis Submitted in Partial Fullfillment of The Requirements of The Degree of Master of Science in Electrical Engineering (Computers and Systems Engineering) 61669

Submitted by

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Bachelor of Computers and Systems Engineering Faculty of Engineering - Ain Shams University 1992

### Under Supervision of

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Cairo -1997

To my family.

# Acknowledgment

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# Approval Sheet

Thesis title:

Scheduling in High-Level Synthesis

Presented by:

Eng.\ Rami Said Abbas

Degree name: Master of Computer and Systems

Engineering

### **Discussion Committee**

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## The Student Profile

#### Name

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### Date and Place of Birth

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### Degree

Bachelor of Computer and Systems Engineering, 1992 - Ain Shams University, Faculty of Engineering, Computers and Systems Engineering Department

### **Profession**

Computer Engineer at Amoco Egypt Oil Company (AEOC), the Information Technology (IT)

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### Statement

This dissertation is submitted to Ain Shams University for the degree of Master of Science in Electrical Engineering (Computers and Systems Engineering).

The work included in this thesis was carried by the author at the Computer and Systems Engineering Department, Faculty of Engineering, Ain Shams University.

No part of this thesis has been submitted for a degree or qualification at other university or institution.

Date: / / 1997

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