1597114

Ain Shams University
Faculty of Engineering

A NEW INETGRATED VERY SENSITIVE MAGNETODETECTOR

لورBy معرف MOHAMMED M. BL-HADY ATYA

J76 JJ

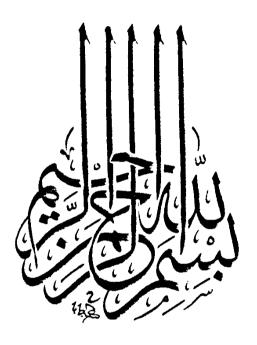
A THESIS

Submitted in partial Fulfillment For The Requirements of The Degree of M.Sc. in Electronics and Computer Engineering



Supervised by Prof. Dr. M. Nabil Saleh
Dr. A.E. El-Hennawy

Cairo-1988





Examiners Committee

Name, Title & Affiliation

Signature

- 1. Prof. Dr. A. A. Talkhan
 Cairo University
- 2. Prof. Dr. A. Kh. Abo-El Seuod
 Alex. University

Almie Khong

3. Prof. Dr. N. N. Saleh
Ain Shams University

Date: / /1988

STATMENT

This dissertation is submitted to Ain Shams University for the degree of M.Sc. in Electronics and Computer Engineering.

The work included in this thesis was carried out by the auther in the department of Electronics and Computer, Ain Shams University, from October 1984 to October 1988.

No part of this thesis has been submitted for a degree or a qualification at any other University or Institution.

Date : Oct. 1988

Signature : HELHADY

Name : Mohammed M. El-Hady

To My Father and Mother,
To My Wife,
To My Kids;
Sameh, Amro and
Marwa.

ACKNOWLEDGEMENT

I would like to express my sincere gratitude to professor Dr. M. NABIL SALEH for his valuable advice, guidance, continous encouragement, best direction, moral support and critical reading of the manuscript. His general consultation is also appreciated.

The auther is also greatly indebted to Dr. A.El-HENNAWY for his significant assistance valuable suggestions, critical remarks, deep discussion in almost all major and minor topics of this thesis, great help in the analysis of the results and precise reading and corrections of the manuscript.

It is to my pleasure to present gratifull thanks to professor Dr. ABD EL-MONEEM BELAL, the director of the National Telecommunication Center, for the facilities and materials allowed by the NTC laboratories which supported our experimental work of this thesis.

ABSTRACT

This work introduces two orginal entirely-integrated CMDS magnetodetectors which are based on carrier heating phenomena associated with the short channel MOSFET devices. The first is based on hot carrier injection from the MOSFET channel into a splitted U-shaped floating gate. The other is based on hot carrier injection into the substrate and the collection of these carriers by two seperate Hall drains. The general approaches to the proposed magnetodetectors are related to the resultant magnetic lorentz force (Hall effect, magnetoresistance effect, carrier deflection). This magnetic lorentz force is produced by the applied magnetic field to be measured. Theory, design considerations, modeling, simulation and experimental results have been inculded. A new and very precise technique employs a MOS transistor which is forced to operate in its weak inversion region of operation. In this case the channel current varies exponentially with the gate to source voltage and the power consumption is very low. These proposed magnetodetectors are characterized by the following features:

- a) Very high sensitivity (40 times greater than that which have been already proposed in Literatures).
- b) Very wide dynamic range of measurement (10 to -3
 10 T).
- c) A single 5v power supply is needed.

A

- e) Compatible with recent IC secalling-down trends.
- f) Automatic scale changing can be provided.
- controlable detector sensitivity.

These mangetodetectors are very needed for the following applications:

- a) Determination and acquisition of the recombination parameters related to the volume or the surface of polcrystalline silicon.
- b) Prediction of anisotropy in thin films.
- c) Ocean and air navidation.
- d) Detection of radiation leakage and
- e) Realization of multilevel digital circuits.

A new circuit configuration for a frequency comparator FC, to be used for detecting the polarity of the applied magnetic field, is also introduced. This frequency comparator is characterized by the following advantages:

- a) Integrated in MOSFET technology on a single chip.
- b) Compatible with VLSI implementation.
- c) Very good linearity.
- d) Very wide dynamic range of operation.

Theory, principle of operation, modeling, simulation and experimental results related to the proposed frequency comparator are also involved.

Our simulation and experimental results show that the sensitivity and the dynamic range of measurement of the proposed magnitetodetectors are dependent on device geometry, biasing conditions, doping profile and surface doping level.

ABBERVIATIONS!

- BFC Magnetic-to-frequency convertor.
- CFC Low-Level current-to-frequency convertor.
- CG Control gate of MOSFET device.
- CMD Classical magnetiodiode.
- CMOS Complementary MOS.
- CVD Chemical vapor deposition.
- DAMS Differential amplification magnetic sensor.
- DDM Dual drain magnetodetector.
- ESFI Epitaxial Silicon Films on Insulator.
- FC Frequency comparator.
- F The magnetodetector output frequency.
- F₁ The output frequency of CFC₁.
- F₂ The output frequency of CFC₂.
- FG Floating gate of MOSFET device.
- HCG Hot-carrier gate-current based magnetodetector.
- MHCG Modified version of HCG magnetodetector.
- MOS Metal-oxide-semiconductor.
- R(F) The frequency controlled resistor.
- SOS Silicon on saphire.
- UJT Unijunction transistor.
- VCC Voltage-to-current converter.
- VLSI Very large scale integeration.

CONTENTS

INTRODUCTION
CHAPTER 1
SURVEY OF TRADITIONAL MAGNETODETECTORS
AND PRESENTATION OF PROPOSED ORIGINAL
MOSFET INTEGERATED ELEMENT.
I. INTRODUCTION4
II. METAL-OXIDE-SEMICONDUCTOR (MOS) HALL DEVICE6
II.1. Electro-Magnetic properties of
silicon MOSFET structure
II.1.a. Variation of Hall voltage
with magnetic field
II.1.b. Effect of L/W ratio on the
Hall voltage10
II.2. Special MOS-Hall devices
III. MAGNETODIODES
TII.1. p nn magnetodiodes
III.2. Schottky magnetodiodes23
III.3. Field effect magnetodiodes24
IV. MAGNETOTRANSISTORS
IV.1. Split-terminal magnetotransistor29
IV.1.a. Double base magnetotransistor29
IV.1.b. Dual collector magnetotransistor30
IV.1.c. Differential amplification
magenetic sensor
IV.2. Unijunction magnetotransistor34

V. PRESENTATION OF NEW CIRCUIT CONFIGURATION FOR
VERY SENSITIVE MAGNETODETECTOR
V.1. Dual Drain Magnetodetector38
V.2. Hot-carrier gate-current based
magnetodetector40
CHAPTER 2
NEW CIRCUIT CONFIGURATION FOR TWO
DIFFERENT MAGNETODETECTORS BASED ON
CARRIER HEATING PHENOMENA.
I. INTRODUCTION43
II. CARRIER HEATNG PHENOMINA45
II.1. Modeling of hot carrier
gate current I46
II.2. Modeling of hot carrier substrate
current I53
III. MINORITY CARRIER TRANSPORT AND
CONDUCTION IN POLYCRYSTALLINE-
SILICON FILMS60
IV. DUAL DRAIN MAGNETODETECTOR (DDM)
IV.1. Circuit configuration66
IV.2. Model
IV.3. Circuit limitation73
IV.4. Modified circuit configuration73
IV.4.a. Magnetodetector73
IV.4.b. Frequency comparator (FC)81
IV.4.c. Model of the frequency comparatorB3
IV.4.d. Transfer characteristics F(B)88

	IV.4.e. Sensitivity study90
٧. ا	HOT-CARRIER GATE-CURRENT BASED
,	MAGNETODETECTOR (HCG).
	V.1. Circuit configuration91
	V.2. Principle of operation93
	V.3. Model94
	V.4. Transfer characteristics F(B)97
	V.5. Sensitivity of the hot-carrier
	gate-current based magnetodetector97
	V.6. Modified circuit configuration
	V.7. Concelsions106
CHAPTER	1 3
	SIMMULATION AND EXPERIMENTAL RESULTS
I.	INTRODUCTION107
II.	EFFECT OF DEVICE PARAMETERS AND BIASING CONDITIONS
	ON DUAL DRAIN MAGNETODETECTOR PERFORMANCE.
	II.1. Dependence of the outpput frequency
	F and device sensitivity S on the
	substrate current I
	II.2. Effect of V , V and DS GS
	channel length L on S111
	II.3. Sensitivity S dependence on the
	Hall mobility µ114
	II.4. The dependence of the sensitivity S on
	the impurity doping concenteration N114 D
III.	1. EFFECT OF DEVICE PARAMETERS AND BIASING

CONDITIONS ON HOT-CARRIER GATE-CURRENT
BASED MAGNETODETECTOR PERFORMANCE
III.1.a. Dependence of F and S on I
III.1.b. Effect of V , V and DS GS
channel length L on S
III.2. EFFECT OF DEVICE PARAMETERS AND BIASING
CONDITIONS ON THE MODIFIED HOT-CARRIER GATE-
CURRENT BASED MAGNETODETECTOR PERFORMANCE
III.2.a. Effect of I on F and S
III.2.b. Effect of V , V and channel DS GS
length L on S
III.2.c. Effect of the Hall mobility μ and
doping concenteration N on S126 D
IV. THE FREQUENCY COMPARATOR SIMULATION AND
EXPERIMENTAL RESULTS
IV.1. Dependence of the value of
the frequency controlled resistor
R(F) on the frequency F
IV.2. Dependence of the frequency comparator
output voltage V on frequencies
F and F130
V. CONCULSIONS132
CHAPTER 4.
CONCULSIONS
IV.1. Summary134
IV.2. Suggestions for future research
REFERENCES140