Ain Shams University

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# Analysis And Design Of Wideband Modulator For Spread Spectrum Systems

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#### Abstract

The aim of this thesis is to study the design procedures of a broadband quadriphase shift keying (QPSK) digital modulator, using finline techniques so as to be applied in spread spectrum systems. Two biphase shift keying modulators, one  $\pi$  and the other  $\pi/2$  are connected in cascade to achieve the required modulation. The obtained modulator has a bandwidth of about 0.5GHz centered at 10GHz with phase deviation less than  $\pm$  7 and a reflection loss variation not more than 0.8db.

The thesis discusses the different spread spectrum modulating techniques, their advantages and disadvantages. A survey is also given about the two main classes of high speed codes used in spread spectrum systems. This survey studies the properties and capabilities of these codes as well as the coders/decoders used for their generation/regeneration. An introduction is then presented to the design of broadband QPSK reflection type modulators used in spread spectrum systems. A modulator is realized with circulators, switching PIN diodes and impedance

matching networks having filter structures. Finally, the practical realization of the QPSK modulator using finline techniques is given. The measurements and results of the modulator circuit using a network analyzer are obtained. It is noticed from the design performance that it agrees quite well with the obtained measured results.

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# Abbreviations

SSS = spread spectrum systems

PSK = phase shift keying

BPSK = biphase shift keying

QPSK = quadriphase shift keying

VCO = voltage controlled oscillator

PN = pseudo noise

DS = direct sequence

FH = frequency hopping

TH = time hopping

DDL = dispersive delay line

## List of Symbols

```
= channel capacity in bits / sec.
C
           = bandwidth in hertz
f
           = frequency in hertz
S
           = Signal power
N
           = noise power
           = power spectral density of bi-PSK
           = power spectral density of quadri-PSK
      (f)
           = noise power spectral density
E<sub>b</sub>
           = energy / bit
T<sub>b</sub>
           = bit duration
Тs
           = symbol duration
Мj
           = jamming margin
Gp
           = process gain
           = system implementation losses
           = compression ratio
D
△ f
          = frequency sweep
          = time increment
46
R
          = information rate
m(t)
          = modulated signal
I(t)
          = in phase data stream
Q(t)
          = quadrature phase data stream
Ī
          = indentity matrix
```

```
G
          = generator matrix
Н
          = parity check matrix
          = syndrome matrix
          = maximum length sequence
          = minimum hamming distance
          = number of correctable errors / codeword
E
          = error sequence
Рe
          = bit error probability
          = symbol error probability
P[e/m(t)] = probability of error assuming m(t) is
          transmitted
          = total series inductance
Ŀs
          = stray capacitance
Ср
^{\rm C}f
         = fringing capacitance
C 4
          = charge storage capacitance
c_d
          = diffusion capacitance
          = capacitance of the i - layer
Сi
          = resistance of the i - layer
Ri
          = total series resistance
Rs
          = depletion layer resistance
Rј
          = switchable impedance element
Ζi
          = matching characteristic impedance
Zm
           = reflection coefficient
```

```
LA
             = maximum db attenuation
  Lm
             = minimum db attenuation
             = db attenuation tolerance
             = Tchebyscheff pass band ripple
 L_{R}
 8
            = load decrement
 R_{A}
            = tuned load resistance
 G_A
            = tuned load conductance
 θ
            = phase angle
            = series admittance of a M network
 Въ
           = shunt admittance of a T network
Вс
           = diode forward resistance
R_{\mathbf{f}}
Rr
           = diode reverse resistance
d_{\mathbf{h}}
           = notch length
           = notch slot width
Yoc
           = open circuit admittance
          = short circuit admittance
Ysc
```

#### Introduction

Spread Spectrum communication systems adopting digital methods to transmit information are rapidly progressing in the technical community. In spread spectrum systems the transmitted bandwidth is enlarged to give enhanced detection, interference rejection, ranging requirements and/or message security in various digital communication, navigation, military and test systems.

Chapter 1 covers the different spread spectrum techniques, their advantages and disadvantages. chapter also classifies spread spectrum techniques according to their four different modulation formats. The most commonly used spread spectrum technique is the direct sequence method associating message security codes. In a direct sequence - spread spectrum system, a carrier in the microwave region is modulated by a digital code sequence. The incoming information signal, which is digitized if it is not in a digital format, is added to a higher speed code sequence. The high speed code sequence dominate the modulating function and hence determines the RF signal bandwidth. The