

Table of Contents

Abstract.....	XI
Table of Contents.....	I
List of Figures.....	IV
List of Tables	VIII
List of symbols	IX
Chapter 1. Introduction.....	- 1 -
1.1. Introduction	- 1 -
1.2. Voltage Source Inverter (VSI) Fed BLDC.....	- 3 -
1.3. Current Source Inverter (CSI) Fed BLDC.	- 4 -
1.4. Literature Survey	- 5 -
1.5. Conclusion.....	- 6 -
Chapter 2. Structure, Modeling and Drive Techniques	- 8 -
2.1. Brushless DC Motor (BLDC).....	- 8 -
2.1.1. Stator.....	- 9 -
2.1.2. Rotor	- 9 -
2.1.3. Dynamic Model	- 10 -
2.2. Hall Sensors.....	- 13 -
2.3. Voltage Source Inverter.....	- 13 -
2.4. Current Source Inverter	- 14 -
2.5. Electronic commutation theory	- 15 -
2.5.1. Block commutation.....	- 16 -
2.5.2. Overlapping technique.....	- 18 -
2.5.3. Phase advance angle technique.....	- 20 -
2.5.4. Proposed technique.....	- 23 -
2.6. Conclusion.....	- 25 -
Chapter 3. Torque Ripple Identification	- 26 -

3.1.	Torque ripple during Normal conduction period	28 -
3.2.	Torque ripple during commutation period	29 -
	Case (1).....	31 -
	Case (2).....	34 -
3.3.	Torque ripple with overlap angle technique.....	41 -
3.3.1.	Overlap region	41 -
3.3.2.	Commutation region	43 -
3.4.	Torque ripple with proposed technique.....	44 -
3.5.	Conclusion.....	45 -
Chapter 4.	Simulation Results	46 -
4.1.	Current source inverter	47 -
4.2.	Voltage source inverter (VSI)	51 -
4.2.1.	Block commutation:.....	51 -
4.2.2.	Overlap technique:.....	54 -
4.2.3.	Phase advance angle	57 -
4.2.4.	Combination between phase advance angle and overlapping technique	62 -
4.2.5.	Phase advance angle and PWM.....	64 -
4.3.	Conclusions	71 -
Chapter 5.	Experimental Work.....	72 -
5.1.	General	72 -
5.2.	Hardware Setup	72 -
5.2.1.	Digital signal processor (DSP)	76 -
5.2.2.	Current Measuring Circuit.....	77 -
5.2.3.	Speed and Position Measurement.....	77 -
5.2.4.	Conditioning Circuits.....	79 -
5.3.	Software Setup	80 -
5.4.	Experimental Results.....	80 -
5.4.1.	System Identification	81 -

5.4.2. VSI Fed BLDC	- 83 -
5.4.3. CSI Fed BLDC	- 88 -
5.5. Conclusion.....	- 91 -
Chapter 6. Conclusion and Future Work.....	- 92 -
6.1. General conclusion	- 92 -
6.2. Future works.....	- 93 -
Bibliography	- 95 -

List of Figures

Figure 1-1 Voltage source inverter fed BLDC motor.	- 4 -
Figure 1-2 Current source inverter fed BLDC motor.	- 5 -
Figure 2-1 Main structure of the system.	- 8 -
Figure 2-2 Trapezoidal back emf.	- 12 -
Figure 2-3 Three Hall sensors with separate magnets mounted on the shaft.	- 13 -
Figure 2-4 Current source schematic.	- 15 -
Figure 2-5 Commutation sequence for clock wise operation.	- 16 -
Figure 2-6 Six state space vectors.	- 16 -
Figure 2-7 Schematic of BLDC drive.	- 17 -
Figure 2-8 Torque output including commutation torque ripple.	- 17 -
Figure 2-9 Three phase stator current with block commutation technique.	- 18 -
Figure 2-10 Switching pattern with overlap angle.	- 19 -
Figure 2-11 Back emf with switching pulse.	- 19 -
Figure 2-12 Three phase stator currents with overlap technique (focused on overlap angle).	- 20 -
Figure 2-13 Stator currents (focused on commutation period).	- 20 -
Figure 2-14 Back emf and pulse signal.	- 21 -
Figure 2-15 Switching pattern with phase advance angle.	- 21 -
Figure 2-16 Three phase stator currents with phase advance angle.	- 22 -
Figure 2-17 Three phase stator currents during commutation.	- 22 -
Figure 2-18 PWM during phase advance angle periods.	- 23 -
Figure 2-19 Back emf with gate pulse.	- 24 -
Figure 2-20 Three phase stator current with proposed technique.	- 24 -
Figure 3-1 Flowchart for torque ripple during normal and commutation periods.	- 27 -
Figure 3-2 Normal conduction.	- 28 -
Figure 3-3 Current Source Inverter fed BLDC	- 30 -
Figure 3-4 During commutation between phase A and B.	- 31 -
Figure 3-5 Commutation between phase A and B ($t_r = t_f$).	- 31 -
Figure 3-6 Commutation between phase A and B ($t_r > t_f$).	- 34 -
Figure 3-7 Commutation between phase A and B ($t_r < t_f$).	- 37 -

Figure 3-8 Energy recovery.	- 39 -
Figure 3-9 Torque ripple with speed.	- 40 -
Figure 3-10 Normal conduction between phase B and C.	- 41 -
Figure 3-11 Three phase conduction during overlap period.	- 41 -
Figure 3-12 Chopping on outgoing phase during advance period.	- 44 -
Figure 4-1: Simulation Strategy.	- 46 -
Figure 4-2 Motor characteristics.....	- 48 -
Figure 4-3 Back emf and gate pulses.	- 48 -
Figure 4-4 Output characteristics.....	- 49 -
Figure 4-5 Stator and DC bus currents.	- 50 -
Figure 4-6 Motor output characteristics.	- 50 -
Figure 4-7 Back emf and Hall signals.	- 51 -
Figure 4-8 Back emf and gate pulses.	- 52 -
Figure 4-9 The motor current and DC bus current.	- 52 -
Figure 4-10 Motor characteristics.....	- 53 -
Figure 4-11: Motor ripples.	- 53 -
Figure 4-12 Back emf and gate pulses with 15 degree overlap.	- 54 -
Figure 4-13 Motor current and dc bus current with 15 degree overlap.....	- 55 -
Figure 4-14 Motor output characteristics with 15 degree overlap.	- 55 -
Figure 4-15 Torque and speed ripples with 15 degree overlap.	- 56 -
Figure 4-16 Speed fluctuation and torque ripples with overlap angle.	- 57 -
Figure 4-17 Stator and DC bus current (with 30° advance angle).	- 58 -
Figure 4-18 Back emf and gate pulse (with 30° advance).	- 58 -
Figure 4-19 Output characteristics (with 30° advance angle).	- 59 -
Figure 4-20 Speed and torque ripples (with 30° advance).....	- 60 -
Figure 4-21 Stator and DC bus control.....	- 60 -
Figure 4-22 Speed and torque ripple with 50° advance.....	- 61 -
Figure 4-23 Speed fluctuation and torque ripples with phase advance angle.	- 62 -
Figure 4-24 Speed fluctuation and torque ripples with phase advance and overlap angle.	- 63 -
Figure 4-25 Back emf and gate pulses (with chopping and 30° advance).	- 64 -
Figure 4-26 Stator and DC bus currents (with chopping and 30° advance).	- 65 -

Figure 4-27 Output characteristics (with chopping and 30° advance).	- 65 -
Figure 4-28 Back emf and gate pulses (with chopping, $\Theta_a=30^\circ$, and $\theta_o=15^\circ$).	- 66 -
Figure 4-29 Stator and DC bus currents (with chopping, $\Theta_a=30^\circ$, and $\theta_o=15^\circ$).	- 66 -
Figure 4-30 Output characteristics (with chopping, $\Theta_a=30^\circ$, and $\theta_o=15^\circ$).	- 67 -
Figure 4-31 Output characteristics with novel switching pattern ($V_{dc}=50V$).	- 68 -
Figure 4-32 Output characteristics with novel switching pattern ($V_{dc}=75V$).	- 68 -
Figure 4-33 Output characteristics with novel switching pattern ($V_{dc}=120V$).	- 69 -
Figure 4-34 Output characteristics with novel switching pattern ($V_{dc}=150V$).	- 69 -
Figure 4-35 Output characteristics with novel switching pattern ($V_{dc}=180V$).	- 70 -
Figure 4-36 Output characteristics with novel switching pattern ($V_{dc}=200V$).	- 70 -
Figure 5-1 Brushless DC motor system.	- 72 -
Figure 5-2 Current source.	- 73 -
Figure 5-3 Inverter circuit.	- 74 -
Figure 5-4 Current source inductors.	- 75 -
Figure 5-5 Clamp circuit.	- 75 -
Figure 5-6 Thyristors drive circuit.	- 76 -
Figure 5-7 DC bus current sensor.	- 77 -
Figure 5-8: Encoder operation.	- 78 -
Figure 5-9: Interface Circuit for Current Measurement.	- 79 -
Figure 5-10: Interface Circuit.	- 79 -
Figure 5-11 Torque conditioning circuit.	- 80 -
Figure 5-12 Motor back emf.	- 81 -
Figure 5-13 Back emf and hall signal.	- 82 -
Figure 5-14 Back emf and encoder index.	- 82 -
Figure 5-15 VSI fed BLDC at no load.	- 83 -
Figure 5-16 VSI fed BLDC with constant load.	- 84 -
Figure 5-17 Output characteristics.	- 85 -
Figure 5-18 VSI fed BLDC with 15 degree overlap angle.	- 85 -
Figure 5-19 Output characteristics.	- 86 -
Figure 5-20 VSI fed BLDC with 30 degree phase advance angle.	- 86 -
Figure 5-21 Output characteristics.	- 87 -

<i>Figure 5-22 VSI fed BLDC with proposed technique.</i>	<i>- 87 -</i>
<i>Figure 5-23 Output characteristics.</i>	<i>- 88 -</i>
<i>Figure 5-24 CSI fed BLDC at no load.</i>	<i>- 89 -</i>
<i>Figure 5-25 CSI fed BLDC at no load with shunt resistor.</i>	<i>- 89 -</i>
<i>Figure 5-26 CSI fed BLDC without snubber circuit.</i>	<i>- 90 -</i>
<i>Figure 5-27 CSI fed BLDC with snubber circuit.</i>	<i>- 90 -</i>
<i>Figure 6-1 Schematic of Z-source inverter.</i>	<i>- 93 -</i>

List of Tables

<i>Table 1-1 Comparison between BLDC and PMSM.</i>	<i>- 2 -</i>
<i>Table 1-2 Switching pattern for clock wise operation.....</i>	<i>- 3 -</i>
<i>Table 4-1 Data for motor to be tested.</i>	<i>- 47 -</i>
<i>Table 4-2 Motor characteristics.</i>	<i>- 47 -</i>
<i>Table 4-3 Relation between phase advance angle and torque ripple.</i>	<i>- 56 -</i>
<i>Table 4-4 Speed and torque ripple with phase advance angle.</i>	<i>- 61 -</i>
<i>Table 4-5 Speed and torque ripple with phase advance and overlap angle.</i>	<i>- 63 -</i>
<i>Table 4-6 Novel switching technique performance at different speeds.....</i>	<i>- 67 -</i>
<i>Table 5-1 BLDC parameters.</i>	<i>- 81 -</i>
<i>Table 6-1 Torque ripple percantage with different techniques.....</i>	<i>- 92 -</i>

List of symbols

$v_a, v_b,$ and v_c	Motor phase voltages.
R	Stator phase resistance.
$L_{aa}, L_{bb},$ and L_{cc}	Phase inductances.
$L_{ab}, L_{bc},$ and L_{ac}	Mutual inductances.
$e_a, e_b,$ and e_c	Instantaneous back emfs.
P	Derivative operator.
δ	Torque angle.
$i_a, i_b,$ and i_c	Motor phase currents.
θ_{ol}	Overlap angle.
θ_a	Phase advance angle.
θ	Rotor position.
EMF.....	Electro motive force.
BLDC.....	Brushless DC.
V_{a1}	Fundamental component of phase voltage.
e_a	stator Back emf of phase A.
E_b	Back emf maximum value.
K_e	Voltage constant.
$f(\theta)$	Represent the per unit shape of the back EMF.
P	Motor output power.
T_l	Load torque.
T_e	Electromagnetic torque.
J	Inertia of the system.
B	Damping constant.
V_s	DC bus voltage.
V_l	Line voltage of the main source.
α	Firing angle.

N (RPM)	Speed (revolution per minute).
ΔN	Speed ripple.
ΔT	Torque ripple.
VSI.....	Voltage source inverter.
CSI.....	Current source inverter.
IPM.....	Intelligent power module.
PA.....	Phase advance technique.
OL.....	Overlap technique.
PAO.....	Combination between phase advance and overlap.
OCPA.....	Overlap and chopping during phase advance period.

Abstract

The high spread of the brushless DC (BLDC) motor and its unique properties makes it a good competitor to the induction motor and the DC motor in many applications. The BLDC motor is outperforming in many aspects but it loses in driving critical load applications due to its torque ripple.

Nowadays, the torque ripple minimization represents one of the main research points. Some researchers produced different motor design techniques to overcome the torque ripples and others produced control algorithms to overcome these ripples. However these new control algorithms are complicated and require many sensors, thus the complexity and cost of the system increase.

A simple, torque ripple minimization, high speed bandwidth and low cost drive are the main scope of this thesis. This is achieved by studying the different driver's techniques and different converters that feed the BLDC motor. Analytical analysis and simulation for a novel switching technique is presented.

The work in this thesis is introduced in six chapters:

Chapter one is divided into three sections. Firstly, provides introductory information about the brushless dc motor (BLDC) and its benefits over the permanent magnet synchronous motor (PMSM). Secondly, the conventional drives associated with torque ripples are illustrated. Finally, the different suggested solutions for output torque ripple minimization in literature are reviewed.

Chapter two studies the complete model of the brushless dc motor (BLDC) drive system and different drive techniques. Firstly, motor modeling, sensor, voltage source inverter, and the current source inverter are illustrated. Secondly, the switching patterns, the basic idea for many drive techniques and novel drive technique are presented.

Chapter three analytical analysis of torque ripple during normal and commutation periods is presented, this analysis contains both torque spikes and torque dips during commutation intervals. The equalization between the current for the incoming and outgoing phases by pulse width modulation, phase advance angle, overlapping technique, and combinations between these techniques are produced. Finally, the conclusion about the best method for minimum torque ripples and speed fluctuation is presented.

Chapter four Illustrate the simulation results of each part of the system discussed above. The simulations are conducted under the same operating point and different drive techniques. The

search for optimum variables to reach minimum ripples is occurred. Then the novel technique with optimum variables is tested under wide speed range.

Chapter five Validates the analysis and the simulation of each detection technique through the experimental results.

Chapter six Concludes the thesis and identifies some areas for future researches.

Chapter 1. Introduction

1.1. Introduction

Pneumatic and hydraulic transmission devices are replaced by electric motors in many applications as electric vehicles and aerospace. Superiority of electric motors over hydraulic and pneumatic systems comes from simplicity, low maintenance, and higher power to weight ratio, small size, low price and ease of control. The dc motor achieves all of these requirements besides excellent output characteristics but the maintenance of commutators and brushes is still a problem. Permanent magnet synchronous machine overcome this problem by replacing the mechanical commutator with an electronic one.

The permanent magnet synchronous machines are produced in two main configurations, where the back emf of the machine may be sinusoidal or trapezoidal. Some scholars define permanent magnet synchronous motor with trapezoidal back emf as Brushless DC motor (BLDC) and permanent magnet synchronous motor with sinusoidal back emf as (PMSM). Other researchers refer BLDC for both types of permanent magnet synchronous machines as in the NEMA Standard MG7-1987, a BLDC motor is defined as "a type of self-synchronous rotary motor controlled by electronic commutation, where the rotor is a permanent magnet with rotor-position sensors, and the related commutation circuit could be either independent or integrated to the motor". The first definition is considered in this thesis

The sinusoidal or trapezoidal back emf waveform comes from the stator distributed winding or concentrated winding respectively. The main benefit of BLDC motor over PMSM is that the power output from the BLDC motor is greater than that of the PMSM with 15% [1]. A comparison between BLDC and PMSM is illustrated in Table 1-1.

Due to PMSM design, torque ripples are produced as cogging torque. Skewing and fractional slot pitch windings are known methods to overcome this ripples. In BLDC, the commutation torque ripple with frequency six times the fundamental frequency is produced besides the cogging torque. This commutation torque ripple is the dominant source of torque ripple in BLDC motor.

The relative ripple is independent of load, varies with speed, and may reach fifty percentage of the average torque. Analytical analysis of commutation torque ripple will be done in chapter three.

Table 1-1 Comparison between BLDC and PMSM.

PMSM	BLDC
• Distributed winding. • Sinusoidal back EMF. • Not self driven. • Power density is lower than BLDC. • Continuous stator flux position variation. • Three phases conduction. • No torque ripples at commutations. • Fewer harmonic due to sinusoidal excitation. • Lower core loss. • Higher switching losses. • Control algorithms are mathematically intensive. • High precision sensor as encoder.	• Concentrated winding. • Trapezoidal back EMF. • Self driven. • Power density is higher than PMSM. • Stator Flux position commutation each 60°. • Two phase conduction. • Torque ripple at commutations. • Low order current harmonics. • Higher core losses due to harmonic content. • Less switching losses. • Control algorithms are simple. • Low cost sensor (Hall Effect).

The BLDC motor has a trapezoidal back emf, for constant output power the square wave current is injected in the motor windings. The commutation between winding (ingoing and outgoing phases) occurs every 60° electrical using electronic switches. But due to the motor inductance the current cannot rise or vanish in no time, this time called commutation time. The different rate of rising and falling time produced torque ripple during this period. Beside the back EMF is not ideal and supply voltage limitations. Thus, commutation torque ripple causes can be summarized as:

- Rectangular current distortion.
- Trapezoidal back EMF distortion.
- Finite dc bus supply voltage.

The distorted back emf is not considered in this work. The work focus on the current distortion causes with assuming ideal back emf and overcoming the finite DC bus supply voltage by current source inverter.

Critical loads driven by BLDC as arm control not allow these ripples. Many researchers are producing different techniques to overcome this torque ripples. The first way is the machine design[2]. The control method, drive technique, and drive circuit are a second way after machine design to overcome the torque ripple. The voltage source inverter (VSI) and current source inverter (CSI) are alternatives to drive BLDC motor. As shown in the next sections.

In the following sections different BLDC driving modes (configurations) as voltage source inverter and current source inverter will be introduced. Moreover different control techniques in each mode are proposed.

1.2. Voltage Source Inverter (VSI) Fed BLDC

The first type of drivers is the VSI, will be discussed in this section. The electronic switches are used instead of commutator to transfer current from one phase to another. The transfer is occurred to maintain the 90° angle between the field magnetic axis and armature magnetic axis thus maximum torque production is occurred. Position sensor is required to know the field magnetic axis position. The main conduction modes are two phase conduction, three phase conduction, and hybrid between two and three phase conduction.

The two phase conduction mode is the first switching strategy for VSI switches. Only one switch is on from upper group (T1, T3, and T5) and one from the lower group (T2, T4, and T6) as shown in Table 1-2. Thus there are six possible switching combinations (commutation) in complete electric cycle, every 60° electrical commutation between phases are occurred. The commutation is occurred depending on the rotor magnetic axis and direction of rotation desired as shown in Table 1-2.

Table 1-2 Switching pattern for clock wise operation

Mode I	Mode II	Mode III	Mode IV	Mode V	Mode VI
$0^\circ < \theta \leq 60^\circ$	$60^\circ < \theta \leq 120^\circ$	$120^\circ < \theta \leq 180^\circ$	$180^\circ < \theta \leq 240^\circ$	$240^\circ < \theta \leq 300^\circ$	$300^\circ < \theta \leq 360^\circ$
s/w 1 & 6	s/w 1 & 2	s/w 3 & 2	s/w 3 & 4	s/w 5 & 4	s/w 5 & 6

Thus, armature magnetic axis is transferred in steps instead of continuous one, causing output ripple torque.

The motor output torque is constant during normal and commutation periods assuming ideal trapezoidal back EMF and ideal square wave line current. However, the actual current does not penetrate the phase in no time and the back EMF is not ideal. Thus the torque ripples are appeared on the motor shaft. The low mechanical time constant, due to low inertia, causes greater speed fluctuation.

The commutation between phases is occurs every 60° as mentioned before. The current decays in same phase and rises up in another one, depend on mentioned sequence for clockwise or anti-clockwise operation, during commutation period. The current rise up rate and decaying rate may